

Transmitter

100/102 of overhead

100

Frame generator

104

108
of FSBs

114
Location of FSBs

116
Value of FSBs

overhead
generator
106

Payload
110

encoder
112

Receiver 100

Frame Receiver 122

overhead

Receiver
of FSBs

location of
FSBs

Value of
FSBs

BER

non-
synch

synch

124

Payload

decoder

synch

Fig. 1

134 → 116

132 → 114

Superframe

OH 1	Data Payload	Parity
OH 2	Data Payload	Parity
OH 3	Data Payload	Parity
OH 4	Data Payload	Parity
OH 5	Data Payload	Parity
OH 6	Data Payload	Parity
OH 7	Data Payload	Parity
OH 8	Data Payload	Parity
OH 9	Data Payload	Parity
OH 10	Data Payload	Parity
OH 11	Data Payload	Parity
OH 12	Data Payload	Parity
OH 13	Data Payload	Parity
OH 14	Data Payload	Parity
OH 15	Data Payload	Parity
OH 16	Data Payload	Parity

Frame 1

OH 17	Data Payload	Parity
OH 18	Data Payload	Parity
OH 19	Data Payload	Parity
OH 20	Data Payload	Parity
OH 21	Data Payload	Parity
OH 22	Data Payload	Parity
OH 23	Data Payload	Parity
OH 24	Data Payload	Parity
OH 25	Data Payload	Parity
OH 26	Data Payload	Parity
OH 27	Data Payload	Parity
OH 28	Data Payload	Parity
OH 29	Data Payload	Parity
OH 30	Data Payload	Parity
OH 31	Data Payload	Parity
OH 32	Data Payload	Parity

Frame 2

OH 33	Data Payload	Parity
OH 34	Data Payload	Parity
OH 35	Data Payload	Parity
OH 36	Data Payload	Parity
OH 37	Data Payload	Parity
OH 38	Data Payload	Parity
OH 39	Data Payload	Parity
OH 40	Data Payload	Parity
OH 41	Data Payload	Parity
OH 42	Data Payload	Parity
OH 43	Data Payload	Parity
OH 44	Data Payload	Parity
OH 45	Data Payload	Parity
OH 46	Data Payload	Parity
OH 47	Data Payload	Parity
OH 48	Data Payload	Parity

Frame 3

OH 49	Data Payload	Parity
OH 50	Data Payload	Parity
OH 51	Data Payload	Parity
OH 52	Data Payload	Parity
OH 53	Data Payload	Parity
OH 54	Data Payload	Parity
OH 55	Data Payload	Parity
OH 56	Data Payload	Parity
OH 57	Data Payload	Parity
OH 58	Data Payload	Parity
OH 59	Data Payload	Parity
OH 60	Data Payload	Parity
OH 61	Data Payload	Parity
OH 62	Data Payload	Parity
OH 63	Data Payload	Parity
OH 64	Data Payload	Parity

Frame 4

Fig. 2

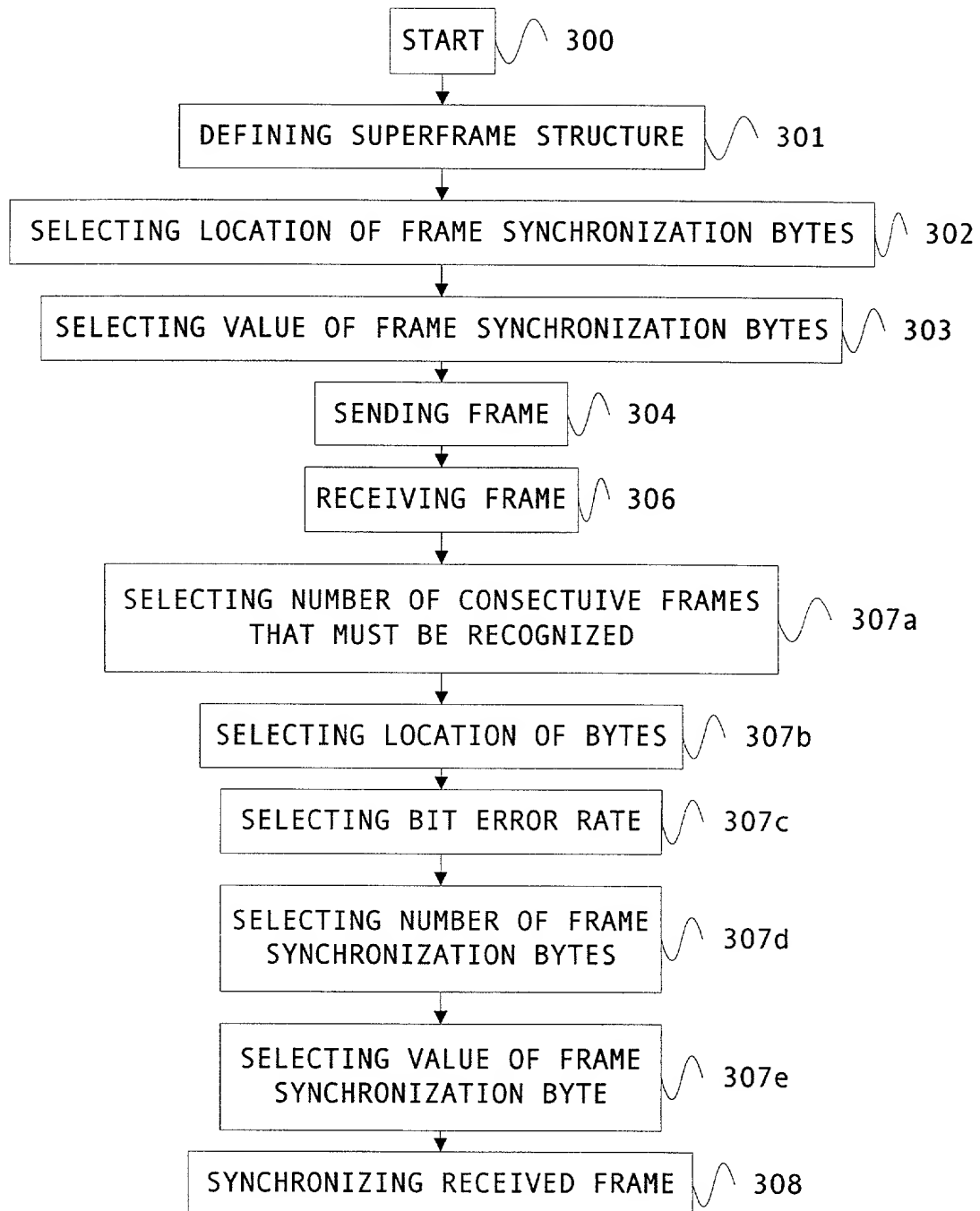


FIG. 4

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graph TD; 200[START] --> 201[PERFRAME STRUCTURE]; 201 --> 202[WITH AN OVERHEAD SECTION]; 202 --> 204[FRAME SYNCHRONIZATION BYTES]; 204 --> 206[FRAME SYNCHRONIZATION BYTES]; 206 --> 210[FRAME SYNCHRONIZATION BYTES];
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The flowchart illustrates the structure of a frame with an overhead section. It begins with a 'START' block (200), which points to a 'PERFRAME STRUCTURE' block (201). This structure is further detailed as 'WITH AN OVERHEAD SECTION' (202). The overhead section is composed of 'FRAME SYNCHRONIZATION BYTES' (204), which are followed by another set of 'FRAME SYNCHRONIZATION BYTES' (206), and finally, a third set of 'FRAME SYNCHRONIZATION BYTES' (210).

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graph TD
    400[START] --> 402[DEFINING FRAME]
    402 --> 404[SELECTING LOCATION OF FRAME SYNCHRONIZATION BYTES]
    404 --> 406[SELECTING BIT ERROR RATE]
    406 --> 408[SELECTIN NUMBER OF FRAME SYNCHRONIZATION BYTES]
    408 --> 410[SELECTING VALUE OF FRAME SYNCHRONIZATION BYTES]

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FIG. 5

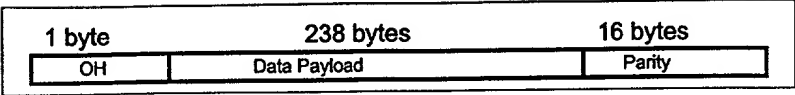


Fig. 6

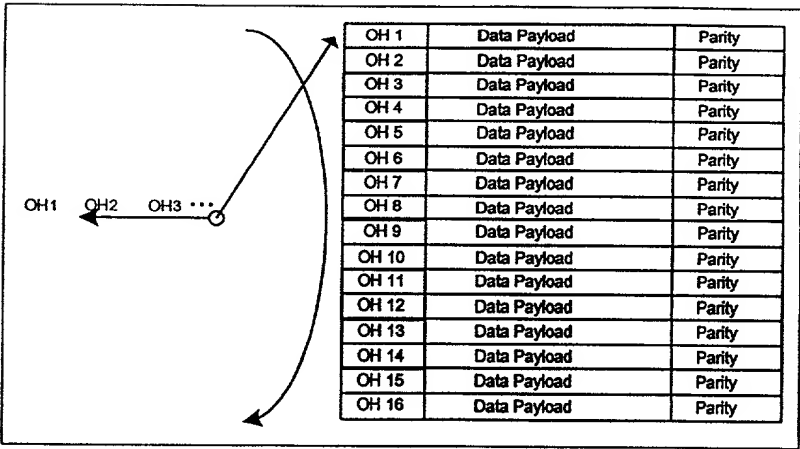


Fig. 7